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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bernd Deutschmann, et al. Art Unit : Unknown
Serial No. : N/A Examiner : Unknown
Filed : Herewith
Title : CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING AN
INTEGRATED SEMICONDUCTOR CIRCUIT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: August 10, 2006

Tonya S. Drake
Tonya S. Drake
Reg. No. 57,861

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

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Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Bernd Deutschmann, et al.	
		Filing Date	Group Art Unit

(37 CFR §1.98(b))

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
/DP/	AA	6,177,298	01/23/01	John H. Quigley			
/DP/	AB	6,618,233	09/09/03	Christian Cornelius Russ et al			
/DP/	AC	5,982,601	11/09/99	Shi-Tron Lin			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
/DP/	AL	WO01/11685	02/15/2001	WIPO				
/DP/	AM	0 822 596	02/04/1998	EPO				
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	
	AR	
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Examiner Signature /Dharti Patel/ (03/04/2009)	Date Considered 03/04/2009
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	